# The Regular Languages in Circuit Classes 

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Joint work with Michaël Cadilhac, Charles Paperman and Thomas Zeume

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## Circuits



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Uniformity $=$ definable in P, LOGSPACE, $\ldots$

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Motivations

P

## VS <br> NP

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P \subseteq P / \text { poly vs } \quad N P
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## algorithms in P <br> with polynomial advice <br> $P \subseteq P /$ poly <br> vS <br> NP

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## algorithms in $P$ <br> with polynomial advice <br> $\mathrm{P} \subseteq \mathrm{P} /$ poly <br> VS <br> NP <br> circuits with <br> polynomially many gates

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## NP

## (Close to hardware)

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Classes of small circuits


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$\underset{\mathrm{M}}{\mathrm{MOD}}$ (number of $1 \equiv_{m} 0$ ?

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\frac{\text { MOD }}{\mathrm{MOD}} \text { : number of } 1 \equiv_{m} 0 ? \quad \underset{\mid \text { MAJ }}{\text { MU }} \text { : number of } 1 \geq \text { number of } 0 ?
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Divide and conquer.

Expressing languages with logic
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$\forall x$, 䎆 $(x) \Rightarrow a(x)$
$\in \Pi_{1}$ [ARB]

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## FO[ARB]

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$\exists^{\text {maj }}=$ a majority of the assignements are satisfying

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## True

- $\Sigma_{1}$
- FO
- FO with prime modular quantifiers


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composite modular gates
(AND $\in \mathrm{CC}_{6}^{0}$ ?)


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It is equivalent to $\Sigma_{2}[A R B]$.

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Circuit lower bound


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Theorem (Pin, Weil)
$\mathcal{L}$ in $\Sigma_{2}[R E G]$ iff:
$\forall u x v \in \mathcal{L}$ such that $x$ can be iterated, then $u x y x v$ is also in $\mathcal{L}$ for every $y$ with the same letters as $x$.

$$
u \quad \text { xyx } \quad v \in \mathcal{L}
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## Proof sketch

Definition (limit (Sipser))
Let $A$ be a set of words in $\mathcal{L}$.
$A$ limit for $A$ is a word $u$ :

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We give here a new method of finding limits, $\underbrace{\text { specially tailored for } \Sigma_{2}}$.
of the form $u x y x v$

## Conclusion

Also in the paper:

- Straubing's conjecture for $\Delta_{2}$.

Not in the paper:

- The proof in its full generality.


## Future work:

- Go higher in the hierarchy: $\mathcal{B} \Sigma_{2}, \Sigma_{3}, \ldots$
- Tackle different kind of fragments, like $\mathrm{FO}_{2}$.

